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September 2008 Special Issue on Circuits for Wireless and Wireline Communication

Journal of Semiconductor Technology and Science will publish a special issue on “Circuits for Wireless and Wireline Communication”. It will provide an overview of recent progress in the development of high speed circuit techniques for wireless and wireline communication aimed at increasing the data bandwidth, improving the circuit speed, lowering power consumption, and so on.

Topics

- Frequency Synthesizers
- Clock and Data Recovery Circuits
- Phase-Locked Loops
- RF Circuits for Wireless Communication
- High-Speed Serial Links
- Equalizers

Authors wishing to have their contributions considered for publication in this Special Issue, which will be published in September 2008 issue, are invited to submit an electronic copy of their manuscript to Editor Prof. Deog-Kyoon Jeong (dkjeong@snu.ac.kr) no later than June 31, 2008. Manuscripts should be submitted in free style double spaced MS word format as an e-mail attachment. See <http://www.jsts.org> for details. Both regular papers as well as letters are welcomed.

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