

Editorial

This is a special issue on 9th Korean Conference on Semiconductors(KCS 2002) held at Cheonan, Feb. 21-22, 2002. Among 360 outstanding papers presented at the Conference, 5 papers have been solicited out of 13 best papers from 13 technical committees for this special issue of JSTS. First paper, authored by K. J. Lee et al., addresses SOC Bus Protocol Checker, especially AMBA AHB protocol. They integrate the AHB protocol checker into Verilog simulation environment for the easy verification of the SOC design. The next paper is related with the FeRAM technology for embedded memory in SOC, authored by H.B Lee et al. Low voltage and reduced active capacitance enable sub-1.5 voltage operation leading to its easy SOC integration. The third paper addresses MMIC drive and power amplifiers covering 6-18GHz authored by H.T. Kim et al.. They use a cascade amplifier for drive amp and modified distributed topology for power amp. The fourth paper authored by B. H. Cho et al., introduces the detail fabrication technology of tunable capacitors and 3-dimensional inductors. The last paper authored by J. H. Lee et al., proposed two lateral field emission diodes using LOCOS and GaN emitter. Their diode shows 2V turn-on voltage and 300 μ A/tip emission current at 25V anode-to-cathode voltage.

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